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		Application Number	10/813,152		
		Filing Date			
		First Named Inventor	Gary A. Kinstler		
		Group Art Unit			
		Examiner Name			
Sheet	1	of	1	Attorney Docket Number	60000500-1024

3/27/07

OTHER ITEMS – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JB	1	TAI et al., COTS-Based Fault Tolerance in Deep Space: Qualitative and Quantitative Analyses of a Bus Network Architecture, 4th IEEE Intl. Symposium on High Assurance Systems Engineering, November, 1999, pp. 1-8.	

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